

In re application of: Seung Jae Chung
Serial No.: 09/864,973

Group: Art Unit 2183

Filed: May 24, 2001

Examiner: Huisman, David J.

For: LOOP INSTRUCTION PROCESSING USING LOOP
BUFFER IN A DATA PROCESSING DEVICE

Date: November 30, 2004

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT TRANSMITTAL FORM

Sir:

Transmitted herewith is an amendment in the above-identified application.

- [] Small entity status of this application under 37 C.F.R. § 1.9 and 1.27 has been established by a verified statement previously submitted.
- [] A verified statement to establish small entity under 37 C.F.R. § 1.9 and 1.27 is enclosed.
- [X] No additional fee is required.

The fee has been calculated as shown below:

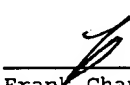
	(Col. 1)	(Col. 2)	(Col. 3)	SMALL ENTITY	OTHER THAN SMALL ENTITY
	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST NO. PREVIOUSLY PAID FOR	PRESENT EXTRA	ADDIT. RATE FEE	OR ADDIT. RATE FEE
TOTAL	24 MINUS	24	=	X 9 \$	X 18 \$ 0
INDEP.	3 MINUS	3	=	X 44 \$	X 88 \$ 0
□ FIRST PRESENTATION OF MULTIPLE DEP. CLAIM				X 150 \$	X 300 \$
				TOTAL	OR TOTAL \$ 0
				ADDIT. FEE \$ 0	

- * If the entry in Co. 1 is less than entry in Col. 2, write "0" in Col. 3.
- ** If the "Highest No. Previously Paid for" IN THIS SPACE is less than 20, enter "20".
- *** If the "Highest No. Previously Paid For" IN THIS SPACE is less than 3, enter "3". The Highest No. Previously Paid For" (Total or indep.) is the highest number found in the appropriate box in Col. 1 of a prior amendment or the number of claims originally filed.

- [] Please charge Deposit Account No. _____ in the amount of \$ _____. Two (2) copies of this sheet are enclosed.
- [] Credit Card Payment Form PTO-2038 for \$ _____ is enclosed.
- [X] Please charge any deficiency as well as any other fee(s) which may become due under 37 C.F.R. § 1.16 and/or 1.17 at any time during the pendency of this application, or credit any overpayment of such fee(s) to Deposit Account No. 50-0679. Also, in the event any extensions of time for responding are required for the pending application(s), please treat this paper as a petition to extend the time as required and charge Deposit Account No. 50-0679 therefor. TWO (2) COPIES OF THIS SHEET ARE ENCLOSED.

Respectfully submitted,

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CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to the: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on November 30, 2004.

Dated: 11/30/04


Frank Chau



2183
PATENT *[Signature]*

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

APPLICANTS: Seung Jae Chung EXAMINER: Huisman, David J.
SERIAL NO.: 09/864,973 ART UNIT: 2183
FILED: May 24, 2001 DOCKET: 8729-206
(IB200010-238)
TITLE: LOOP INSTRUCTION PROCESSING USING LOOP
BUFFER IN A DATA PROCESSING DEVICE

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

AMENDMENT

In response to the Notice of Non-Compliant Amendment dated November 26,
2004, please amend the above-identified application as follows:

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence (and any document referred to as being attached or enclosed) is being deposited with the United States Postal Service as first class mail, postage paid in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, Alexandria, VA 22313 on the date indicated below:

Date: 11/3/04

By: *[Signature]*

Frank Chau

In the Title:

Please replace the title with the following:

Loop Instruction Processing Using Loop Buffer In A Data Processing Device
Having A Coprocessor.

In The Drawings:

In Fig. 1, the word “coprocessor” in box 120 and the word “program” in box 130 were misspelled. These words are now correctly spelled in the amended Fig. 1.

In the Abstract

Please replace with the following:

A data processing device comprising a central processing unit (CPU) for fetching instructions from a program memory, decoding the instructions and sending a signal (CCLK) to a coprocessor if a coprocessor type instruction is decoded; a coprocessor for decoding the coprocessor-type instructions upon receipt of the signal (CCLK); and a loop buffer for receiving from the program memory instructions within a loop and storing the instructions within the loop when the coprocessor decodes a loop operation from the coprocessor-type instructions, wherein the instructions within the loop are retrieved from the loop buffer for execution in a subsequent iteration of the loop, wherein a disable signal is sent to the program memory for inhibiting access of the program memory while the instructions within the loop are retrieved from the loop buffer.